

Delhi Institute of Technology and Management, Sonipat

Assignment No. – I

Subject: Switching Theory and Logic Design

Time: Submit within 7 days

Subject Code: ETEC 205

Max. Marks: 30

Date: 01.11.2021

Note: Attempt any five questions. All questions carry equal marks.

1. Convert Hexadecimal into Octal

(a) B9F.AE16

(b) A23B.E56

2. Minimize the logic Function

$$Y (A,B,C,D) = \sum m (0,1,2,3,5,7,8,9,11,14)$$

3. Simplify the Following Expression using by Don't Care Conditions

$$Y (A,B,C,D) = \sum m (1,4,8,12,13,15) +d (3,14)$$

4. Minimize the SOP Expression for following logic Function

$$Y (A,B,C,D) = \sum m (2,7,8,9,10,12)$$

5. Minimize using Q-M Method

$$Y (A,B,C,D) = \sum m (0,1,2,3,5,7,8,9,11,14)$$

6. Minimize using Q-M Method

$$Y (A,B,C,D) = \sum m (0,5,6,12,13,14)+ d (2,4)$$

7. Realization of 2:1 Mux using Gates.

8. Realization of 4:1 Mux using Gates.

**Prepared By:
Mr. Gyanender Kumar**